# Towards an efficient implementation of PME on low-bandwidth Linux clusters

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## Abstract / poster guide

Particle-Mesh-Ewald<sup>2,3,4</sup> (PME) is an efficient method for calculating the long-range part of the electrostatic forces in molecular dynamics (MD) simulations. A protein embedded in a box of water molecules is a typical example of such an MD system which then consists of several 100 000s of atoms. A numerical simulation that covers a significant time-span (to reveal protein unfolding for example) can that covers a significant time-span (to reveal protein timologing for example) can take weeks to months even on a modern parallel computer. Parallel PME involves a lot of inter-process communication and therefore soon turns out to be the bott-lencek, particularly for larger CPU numbers. The GROMACS<sup>1</sup> MD simulation program scales reasonably well up to 20 CPUs on computers with a fast inter-connect like the IBM Regata. On a standard Linux cluster with gigabit ethernet however, scaling breaks down already at 4 or 8 CPUs (Fig. 9, black line).

Our goal is to enhance the PME performance on parallel computers without expensive network hardware. Therefore two modifications of the algorithm are being implemented that both minimize the time expensive inter-process commu-

A The atom decomposition which is used in GROMACS (Fig. 1) is changed into a domain decomposition for the PME part (Fig. 2). This way less communication is needed and time gained that clearly outweighs the time spent for reordering the atoms (Fig. 9, green line).

B. PME is moved to a processor subgroup while the remaining CPUs calculate the short-range interactions (Fig. 3). The parallel FFT, which is required twice for each PME step, is known to scale much better on a smaller number of processors. At a high number of CPUs the time gain during the FFTs outweighs the time loss for transferring the particle data between the processor groups (**Fig. 9, blue line**). Small MD systems gain speedup with PME/PP division even on a small number of CPUs (Figs. 7-8).

# **GROMACS 3.2.1** parallel PME

The original implementation of the parallel PME algorithm requires for each CPU one MPI\_Reduce operation to sum the individual contributions to the charge grid. After the FFT one MPI\_Beast per CPU is done to broadcast the potential grid to the other CPUs. Fig. 4 shows how time-expensive these collective operations are.



Fig. 1: Original parallel PME scheme as implemented in GROMACS 3.2.1

# Gromacs 3.2.1 time step (4 CPUs)



Fig. 4: Analysis of a single time step: Time (s) on horizontal axis, CPU number on vertical axis. CPUs: Intel 3.06 GHz Dual Xeons, network: gigabit ethernet, MD system consists of approx, 80000 atoms

#### A small test system (4000 atoms, 6 CPUs)



Fig. 7: Time step for a 4000 atom system with domain decomposition only. Spee dup on 6 CPUs is 2.8. A significant part of the time is lost by redistributing the particles and forces



Fig. 8: Same as Fig. 7, but with domain decomposition and PME/PP splitting. A speedup of 3.6 can be achieved, because particle redistribution is much faster on just 2 CPUs while the FFT is not much slower.

## Particle-Mesh-Ewald (PME)



# A. Domain decomposition in PME part



Fig. 2: Parallel PME with domain decomposition as implemented by J. Pichl-

### Domain decomposition time step (4 CPUs)



Fig. 5: Same as Fig. 4, with PME domain decomposition. The time step is much ter, as the summation of the charge grids and the broadcast of the potential grid (compare Fig. 4) were replaced by exchange of grid boundaries (C, P).

#### Speedup comparison (80000 atoms)



Fig. 9: Speedups for different PME implementations on Intel Dual Xeons 3.06 GHz with gigabit ethernet. The test system is a protein embedded in a 1 membrane surrounded by water and consists of approx. 80 000 atoms. edded in a lipid bilaye

 $V_{rec}$  needs the Fourier transformed charge density. To be able to use the discrete Fourier transformation (FT) the charges are spread on a grid:



Each charge is distributed among its 4x4x4 = 64 neighbouring grid points. The charge mesh has a spacing of 0.12 nm and is a good approximation for the distributed bution of charges at the particle positions.

The Coulomb forces are evaluated in the following way each time step (The color scheme corresponds to that of Figs. 4-8):

- a Calculate short-range Coulomb forces F: 4:b Calculate long-range Coulomb forces with PME:
  - 1 put charges on grid
- 2 FFT charge grid convolution FFT back yields potential V<sub>rec</sub> at grid points
- $\mathbf{F}_{i,rec} = -d/d\mathbf{r}_i \ V_{rec}$

## B. Splitting of CPUs into PME and PP

The second improvement is to additionally split the parallel computer into a group of CPUs which calculate the short range (Particle-Particle, PP) Coulomb forces and a group of CPUs which do the long range (PME) part.

At the start of a time step, the PP CPUs send the particle data to the PME CPUs which at the end of the time step send back the long-range forces.



Fig. 3: PME/PP division scheme. Each PP CPU sends its particle data directly to the PME CPU which needs it to process the long-range Coulomb forces

### PME/PP splitting time step (4 CPUs)



Fig. 6: Same as in Fig. 4, but with domain decomposition and PME/PP splitting. Short-range forces are done by PP CPUs 1 and 2, long-range by PME CPUs 3 and 4.

#### Summary

Achievements for gigabit ethernet:

- with domain decomposition: scaling @ 4 CPUs & 80 000 atoms 0.74 ! (was 0.54)
- domain decomposition and node splitting: speedup of > 10 possible ! (was: 2.5)
- · PME/PP splitting necessary for small systems on many CPUs
- · Outlook: speed-up splitting code (e.g. by using nonblocking communications)

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